

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S70	1	(array with programmable same pre-diffused same memory same (plurality multiple amy more ) same interface same control\$4 same memory with type same (interconnect\$3 connect\$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:48
S69	0	716/6.ccls. and pre-diffused and memory same interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:44
S65	578	716/16.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:34
S68	0	pre-diffused and memory same type same interface and 716/15.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:20
S67	205	716/15.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:10
S66	0	pre-diffused and memory same type same interface and 716/16.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:02
S64	631	716/17.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:50
S63	0	716/17.ccls. and pre-diffused and memory and interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:42

## EAST Search History

S56	631	716/17.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:39
S62	5	pre-diffused and memory same type and interface and control\$4 and array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:34
S61	2	pre-diffused and memory adj type and interface and control\$4 and array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:34
S60	9	pre-diffused and memory and interface and control\$4 and array	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:33
S59	12	pre-diffused and memory and interface and control\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:32
S58	11	pre-diffused and memory and interface and control	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:28
S57	18	pre-diffused and memory and interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 17:28
S55	1	S50 and S54	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:40

## EAST Search History

S54	277	(plurality many more ) with (block logic circuit sub-circuit subcircuit portion) same (interface near4 memory) same (control\$3 access\$3) same type with memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:40
S52	24	S45 and S46 and S47 and S48 and S49	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:35
S51	48	S45 and S46 and S47 and S48	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:32
S50	105	S45 and S46 and S47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:32
S49	136872	(control\$3 access\$3) with memory same type	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:32
S48	48036	(memory adj interface peripheral adj (circuit logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:30
S47	26883	memory with core	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:27
S46	114291	array with cell	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:27

## EAST Search History

S45	3556	metal with programmable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 09:26
S44	204	716/15.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:53
S43	0	pre-diffused and memory adj interface and 716/16.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:52
S42	576	716/16.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:52
S40	0	pre-diffused and memory adj interface and 716/17.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/13 09:38